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## APPLICANTS

John C. Malinowski, Jericho, VT;

Edmund J. Sprogis, Underhill, VT;

Steven H. Voldman, So. Burlington, VT;

\*\* CONTINUING DATA \*\*\*\*\*

(None) LT

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

(None) LT

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35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>LT</i>			

## ADDRESS

30449

SCHMEISER, OLSEN + WATTS

3 LEAR JET LANE

SUITE 201

LATHAM, NY

12110

## TITLE

Dual chip stack method for electro-static discharge protection of integrated circuits

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